

Curriculum Vitae

(May 2019)

Carlo Samori, IEEE Fellow

Present

Full Professor at Politecnico di Milano

In 2019 teaches the course Mixed-Signal Circuits Design, Master Degree in EE, Politecnico di Milano.

Education

1992 Laurea Degree (summa cum Laude) in Electrical Engineering at Politecnico di Milano, Italy.

1995 PhD in Electrical Engineering and Communications, at Politecnico di Milano, Italy.

Research activity and publications

Analog/mixed signal/RF design in scaled CMOS technologies. Presently, the main activity is in the area of frequency synthesis: low-phase noise integrated oscillators, digitally-intensive Phase locked loops (PLLs) in 28nm CMOS technology.

Up to May 2019 co-author of about 130 international peer reviewed publications, h-index=28 (<https://www.scopus.com/authid/detail.uri?authorId=35562226900&origin=cto>).

Coauthor of the book "Integrated Frequency Synthesizers for Wireless Systems", Cambridge University Press, 2007, and of several books' chapters.

Holds 4 patents.

IEEE Activities

Member of the Technical Program Committee of ISSCC, 2012 – 2016.

Member of the Technical Program Committee of ESSCIRC, 2013 -2018.

Guest Editor of JSSC, December 2014.

Tutorial Lecturer at ISSCC 2016: "Understanding phase noise in LC oscillators"

Forum Lecturer at ISSC 2017: "High performance frequency generation for wireless and wireline systems".

IEEE Webinar Lecturer in December 2017.

IEEE Distinguished Lecturer, 2016-2017. Lectures at:

University of Texas Austin, University of Toronto, Georgia Institute of Technology (Atlanta, GA), Princeton University.

IEEE Fellow, from 2017.

Consulting activity and industrial partnerships

Bell Labs – Murray Hill, NJ, USA, 1997 - 2002

ST Microelectronics, Italy, from 1995 to present in several sites (Catania, Milano)

Ericsson Labs, Italy, 1997 – 2001

Intel Labs, Hillsboro, Or, USA, 2006-2010, 2016

Infineon Technologies, Villach, AT, 2016 - present