

Curriculum Vitae



Personal information

First name/ Surname

Marcello Mariani

Work experience

Dates

1/2015 → now

Occupation or position held

Senior Member of Technical Staff, Emerging memories R&D

Main activities and responsibilities

In charge of process integration, from proof of concept to pre-industrialization phase, of emerging memories technologies, with a particular focus on reliability.

Name and address of employer

Micron Technology, Via Trento 30, Vimercate, Italy

Type of business or sector

Semiconductors

Dates

7/2013 → 1/2015

Occupation or position held

Senior Member of Technical Staff, Process Integration Team Leader

Main activities and responsibilities

Leading a group in charge of process integration for advanced memories. Main responsibilities are:

- Technology roadmap definition for advanced memories, with a particular focus on process integration
- Advanced memories process integration development, from architecture foundation to pre-industrialization phase
- Coordination of process development "task forces", including cooperation with external research institutes and/or universities
- Process transfer from R&D to production sites.

Name and address of employer

Micron Technology, Via C. Olivetti 2, 20041 Agrate Brianza (Italy)

Type of business or sector

Semiconductors

Dates

01/09/2008 → now

Occupation or position held

Contract Professor

Main activities and responsibilities

Running the "Microelectronic technologies" course for EE graduate students.

Name and address of employer

Politecnico di Milano, Piazza L. da Vinci, 32 - 20133 Milano (Italy)

Type of business or sector

University

Name and address of employer

Politecnico di Milano, Piazza L. da Vinci, 32 - 20133 Milano (Italy)

Type of business or sector

University

Dates

01/10/2000 → 30/09/2002

Occupation or position held

Process development engineer

Main activities and responsibilities

Development of plasma etching processes.

Name and address of employer

ST Microelectronics, Via C. Olivetti 2, 20041 Agrate Brianza (Italy)

Type of business or sector

Semiconductors

Dates **01/02/2000 → 09/10/2000**
Occupation or position held **Researcher**
Main activities and responsibilities In charge of designing and testing micro-strip silicon detectors for the CMS (Compact Muon Solenoid) experiment, that is now taking place at the LHC (Large Hadron Collider) accelerator in CERN (European Centre for Nuclear Research), Geneva.
Name and address of employer INFN (National Institute for Nuclear Physics) Edificio C - Polo Fibonacci Largo B. Pontecorvo, 3 - 56127 Pisa
Type of business or sector University

Dates **01/10/1999 → 31/12/1999**
Occupation or position held **Researcher**
Main activities and responsibilities Worked in prof. Fukuda's lab on crystalline fibres growth by micro-pulling-down technique. Growth of YAG and LuAG fibres for laser applications.
Name and address of employer Institute for Materials Research, Tohoku University, 2-1-1 Katahira, Aoba-ku, Sendai 980-8577, Japan
Type of business or sector University

Education and training

Dates **01/10/1994 → 27/09/1999**
Title of qualification awarded Degree in Physics (full marks, 110/110)
Principal subjects/occupational skills covered Solid state physics, physics of semiconductor devices. Thesis title "Automatic quantum efficiency measurements on commercial CCD cameras".
Name and type of organisation providing education and training Università degli studi di Pisa
Level in national or international classification ISCED 5

Personal skills and competences

Mother tongue(s) **Italian**

Other language(s)

Self-assessment

European level (*)

English

Understanding				Speaking				Writing	
Listening		Reading		Spoken interaction		Spoken production			
C2	Proficient	C2	Proficient	C2	Proficient	C2	Proficient	C2	Proficient

(*) [Common European Framework of Reference for Languages](http://www.cedefop.europa.eu)